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Commissioner for Patents
MAIL STOP: Amendment
P.O. Box 1450, Alexandria, Virginia 22313-1450
TRANSMITTED TO USPTO MAIN FACSIMILE NO. 703-872-9306

RE: Tomohiro TAIRA *et al.*, US Appl'n No. 10/670,352
Att'y Docket No. 501.41197CX1
Examiner A.C. Stevenson, AU 2812, USPTO Conf. No. 5276

SUBMISSION OF AMENDMENT

Sir:

Applicant hereby submits the attached AMENDMENT (11 pages) for entry in
the above-noted application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached AMENDMENT (11 pages) is being
FORMALLY TRANSMITTED via the USPTO Main Facsimile No. 703-872-9306 on
2 May 2005.


Patricia J. Jaquet

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501.41197CX1/310100259US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Tomohiro TAIRA *et al.*
Serial No. : 10/670,352
Filed : 26 September 2003
For : FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE AND TESTING METHOD
Group AU : 2812
Examiner : A.C. Stevenson
Conf. No. : 5276

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AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

2 May 2005

Sir:

In response to the Office Action mailed 1 February 2005 in connection with the above-identified application, the following amendments and remarks are respectfully submitted.

In accordance with 37 CFR §1.121 in the Final Rule effective 30 July 2003, and as revised in the Final Rule effective 21 October 2004, each section of amendment begins on a new page, and changes are shown by strike-through (or double brackets where appropriate) and underlining to indicate deletions and additions, respectively. A complete listing of all claims ever presented in the application is given with the current status of each claim, and only the text of all pending and withdrawn claims is presented in full, with those pending/withdrawn claims not being amended herein being presented in clean version.